

ABSTRACT OF THE INVENTION

A bi-directional communication link has plural channels with respective masters and slaves at respective ends of respective channels, in which each master issues a Master Tx clock, each slave constructs a Slave Rx clock
5 frequency locked to the Master Tx clock, and a Slave Tx clock frequency locked to the Slave Rx clock. A metric processor for each master produces a metric signal indicative of resolution of a signal received by the master from the corresponding slave. A decision processor responsive to said
10 metric processor changes the phase of the Slave Tx clock relative to the Slave Rx clock so as to maximize the metric signal. In one embodiment, the resolution is a resolution between leading and trailing edges of the received signal. In another embodiment, the resolution is a resolution between
15 allowed amplitude levels of the received signal.